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| Notice of References Cited | Application/Control No. 09/826,427 | Applicant(s)/Patent Under Reexamination CAVALLI, DIDIER | |
| | Examiner Barry J. O'Brien | Art Unit 2183 | Page 1 of 1 |

U.S. PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Name | Classification |
|---|---|--------------------------------------------------|-----------------|--------------------|----------------|
| | A | US-4,484,271 | 11-1984 | Miu et al. | 712/244 |
| | B | US-5,822,595 | 10-1998 | Hu, King Seng | 710/264 |
| | C | US-6,708,241 | 03-2004 | Futral, William T. | 710/260 |
| | D | US- | | | |
| | E | US- | | | |
| | F | US- | | | |
| | G | US- | | | |
| | H | US- | | | |
| | I | US- | | | |
| | J | US- | | | |
| | K | US- | | | |
| | L | US- | | | |
| | M | US- | | | |

FOREIGN PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Country | Name | Classification |
|---|---|--------------------------------------------------|-----------------|---------|------|----------------|
| | N | | | | | |
| | O | | | | | |
| | P | | | | | |
| | Q | | | | | |
| | R | | | | | |
| | S | | | | | |
| | T | | | | | |

NON-PATENT DOCUMENTS

| * | | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages) |
|---|---|-------------------------------------------------------------------------------------------------------------------------------------------|
| | U | Short, Embedded Microprocessor System Design: An Introduction Using the Intel 80C188EB, 1998, Prentice-Hall, Inc., pgs. 219-224, 467-470. |
| | V | |
| | W | |
| | X | |

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.